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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10010994	FILING DATE 11/12/2001	CLASS 707	SUBCLASS 711	GAU 2476	EXAMINER M.K.M.
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**\*\*APPLICANTS:** Osborne Randy; 154 2185

**\*\*CONTINUING DATA VERIFIED:**

**\*\* FOREIGN APPLICATIONS VERIFIED:**

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		004667.P12472
Verified and Acknowledged Examiners's initials		
TITLE : Method and apparatus for read launch optimizations in memory interconnect		

U.S. DEPT. OF COMM. / PAT. & TM-PTO-426L (Rev. 12-94)

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<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Application Examiner	
		<b>PREPARED FOR ISSUE</b>	
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